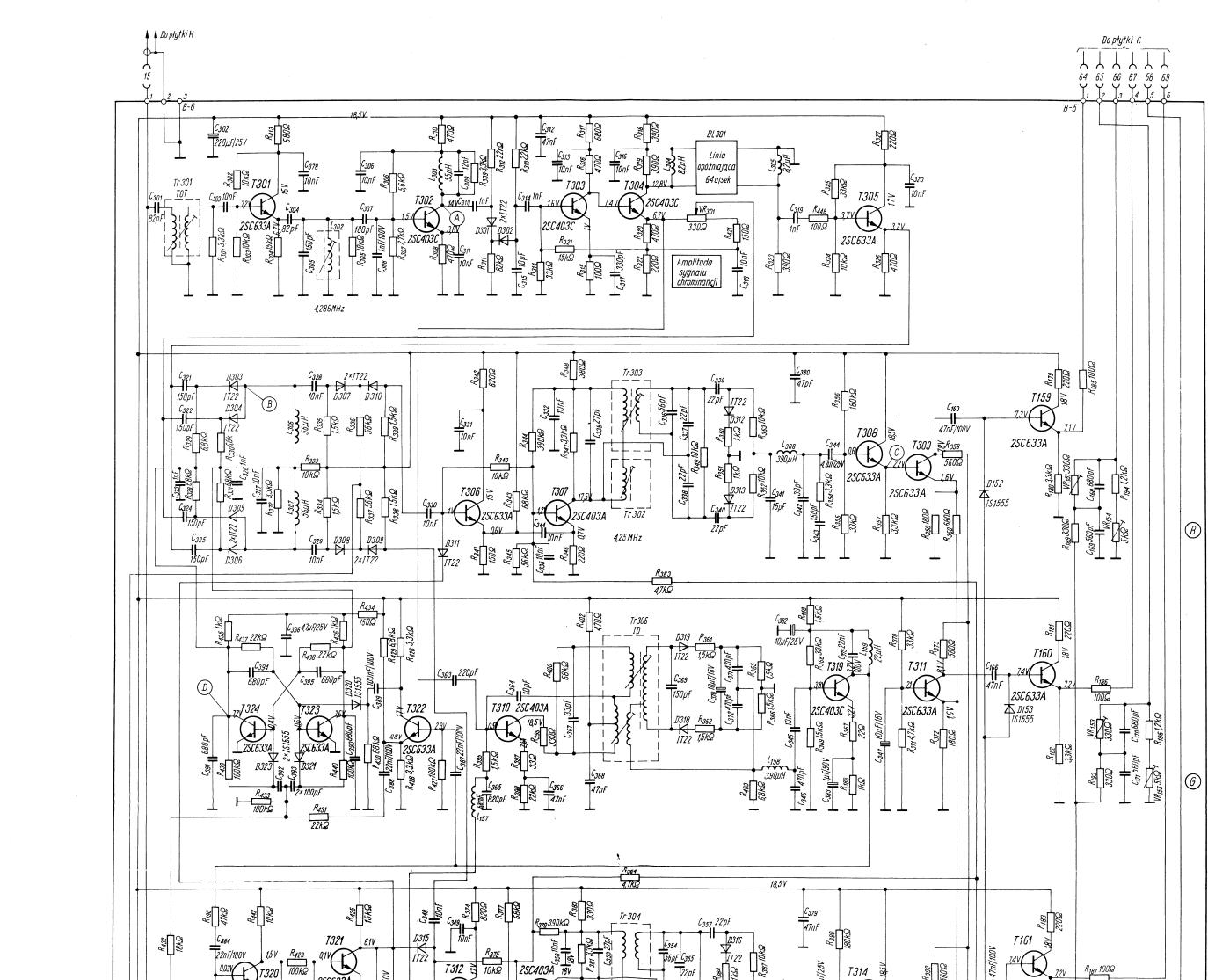
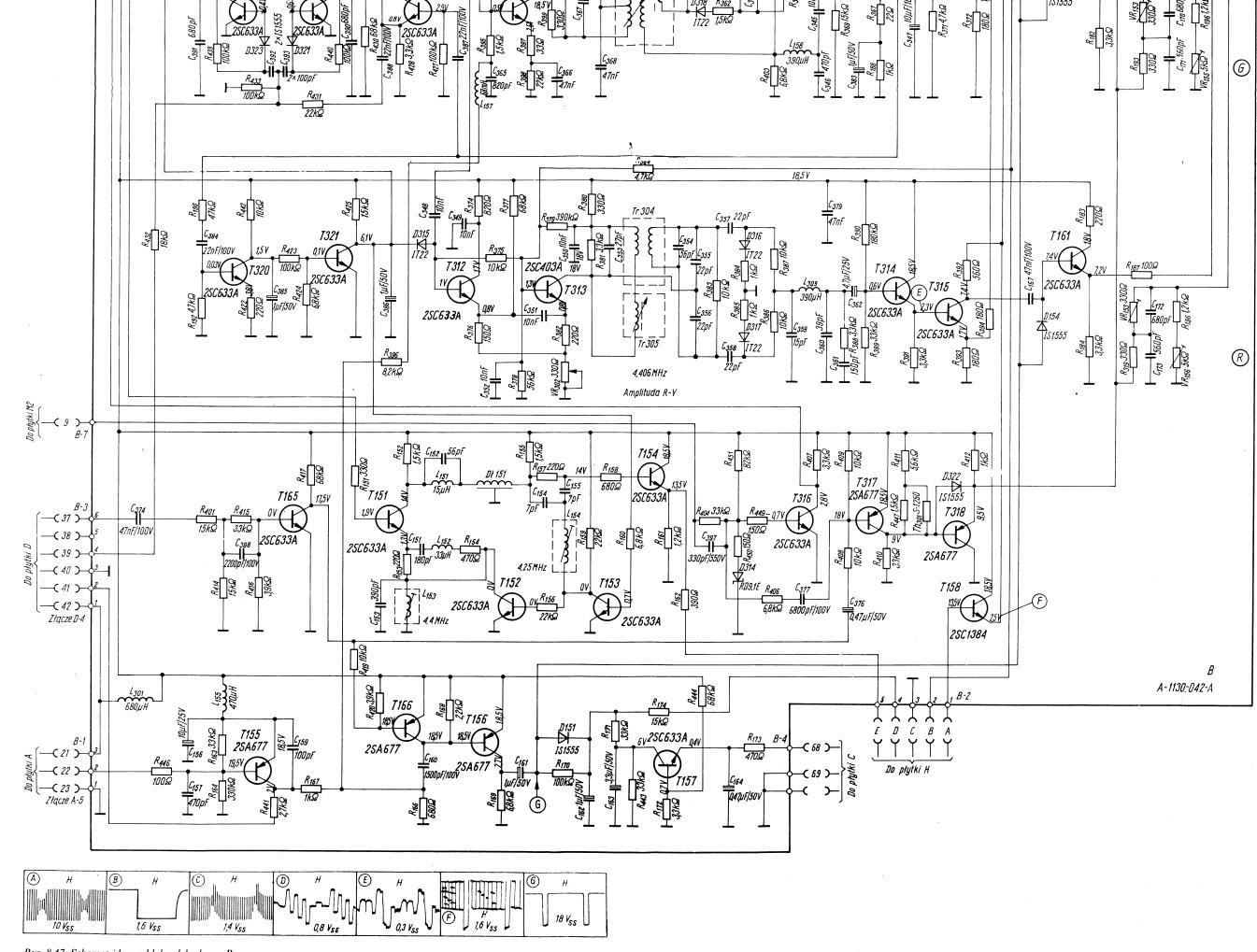
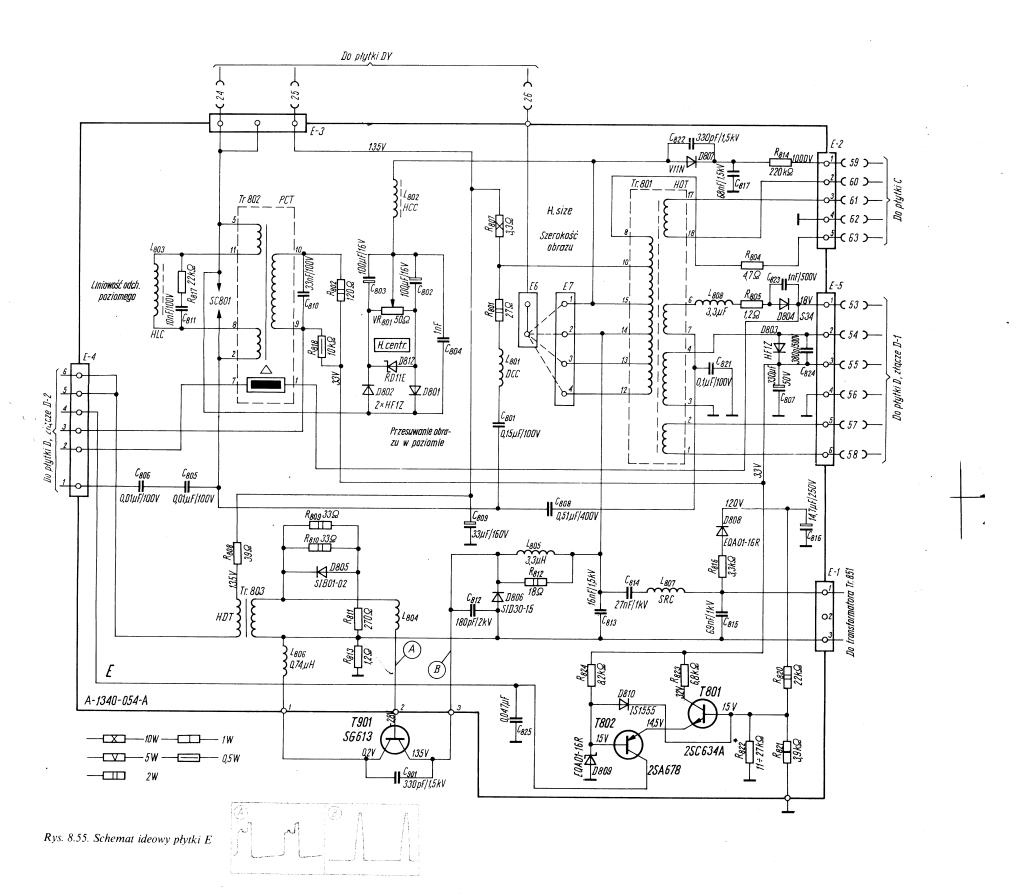
MODEL

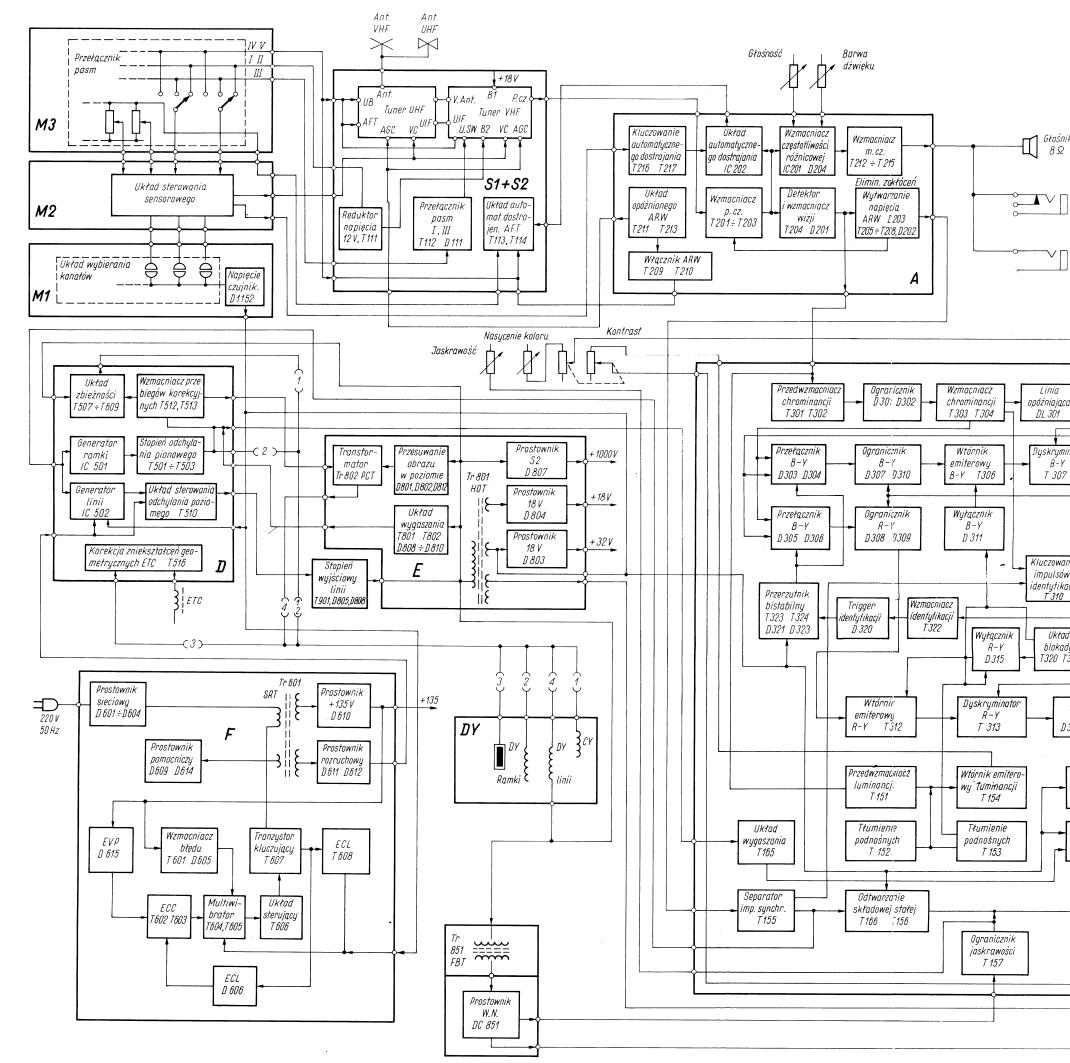




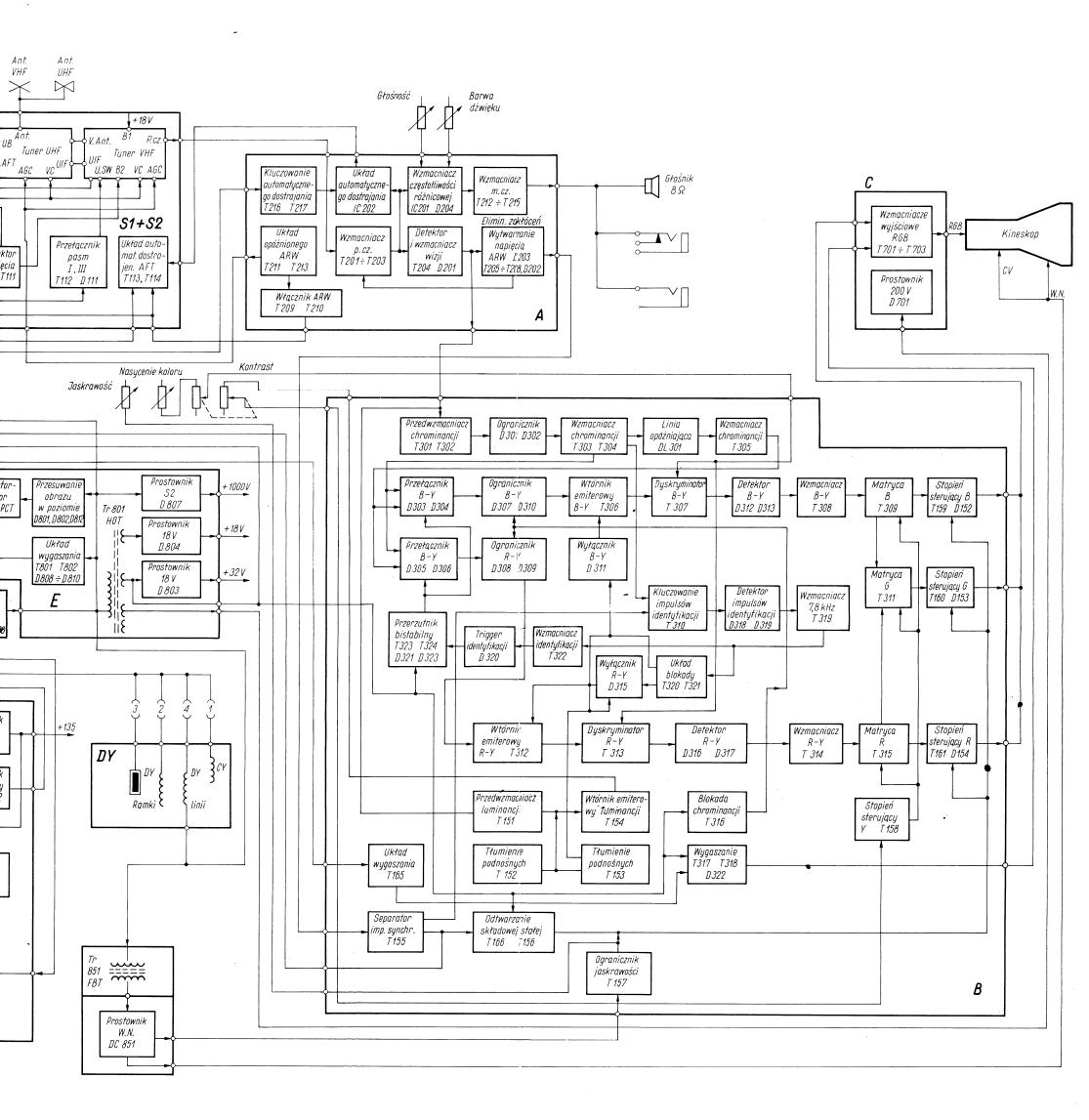
Rys. &47. Schemat ideowy bloku dekodera – B

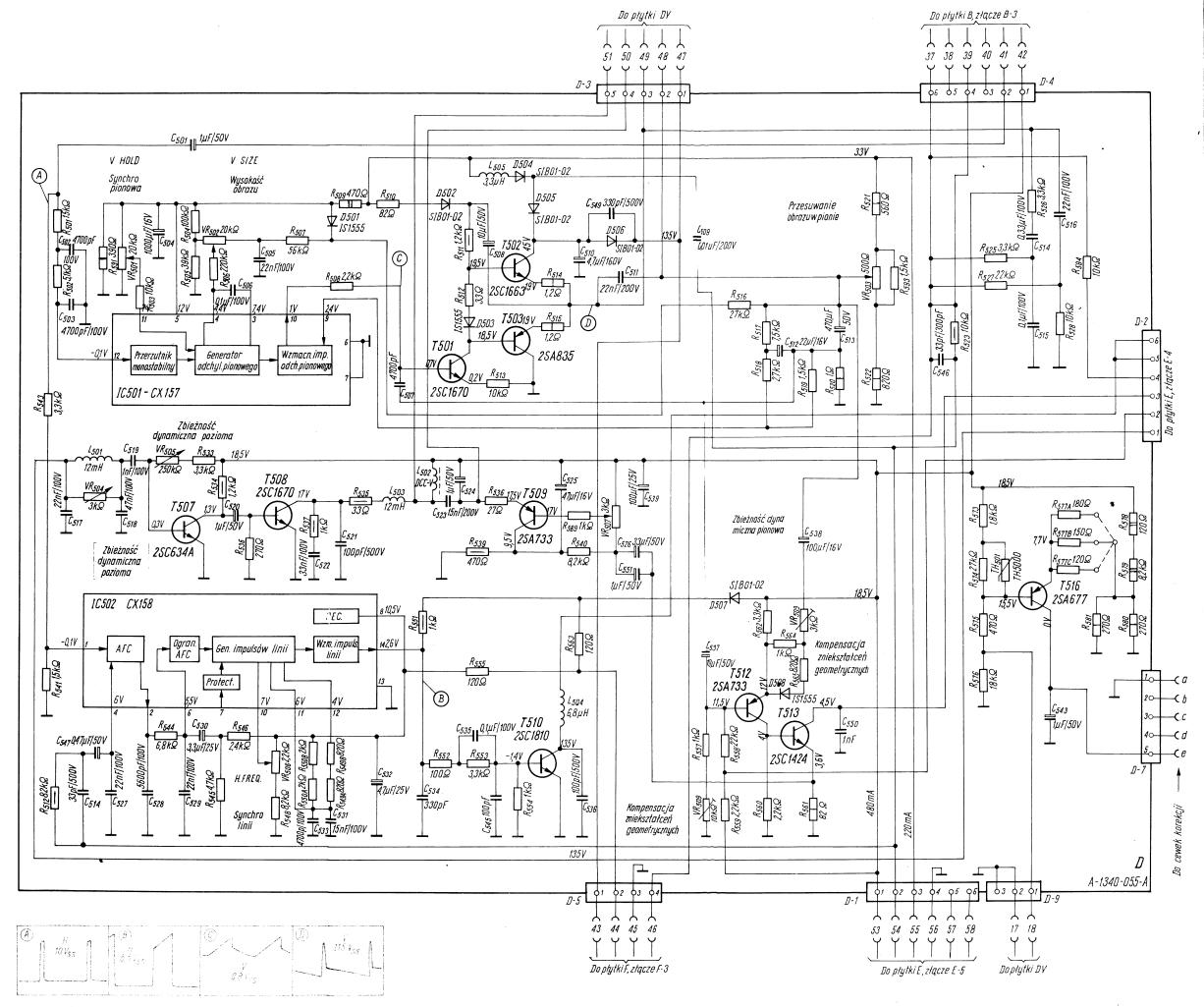




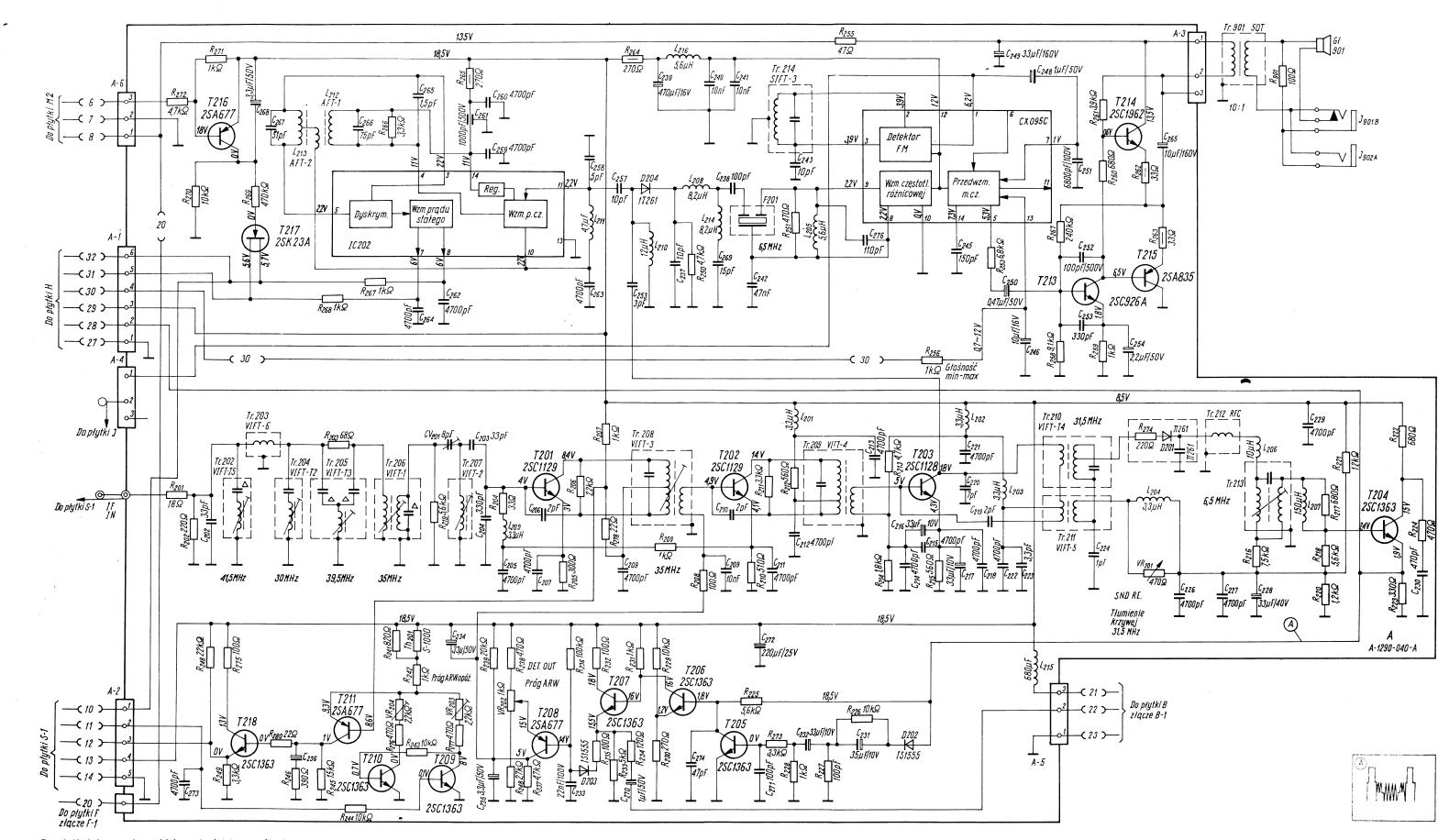


Rys. 8.38. Schemat blokowy odbiornika SONY typu KV 1820 R





Rys. 8.54. Schemat ideowy płytki D



Rys. 8.45. Schemat ideowy bloku pośredniej częstotliwości – A